

FIG. 1

Figure 1 is a schematic diagram of a display device. It consists of a rectangular frame (12) that surrounds a central white rectangular area (13). A checkered border (14a) is positioned between the frame and the central area. A thick black border (14) is located on the outermost edge of the device. An arrow (11) points to the top edge of the frame.

FIG. 2a

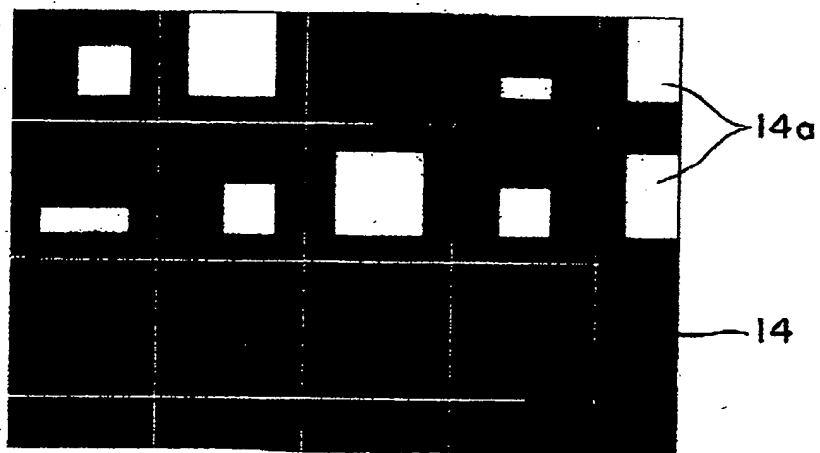


FIG. 2b

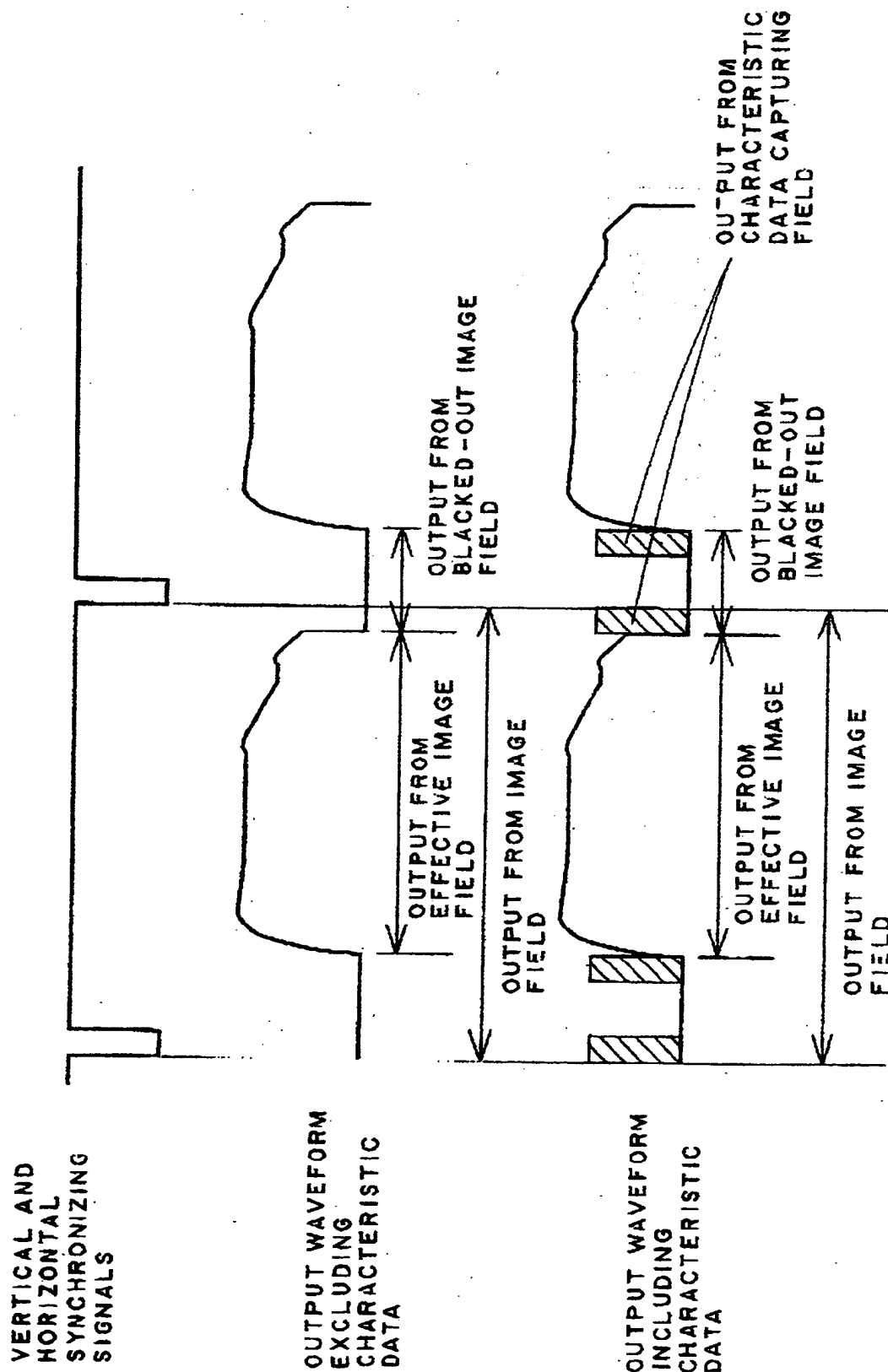


FIG. 3

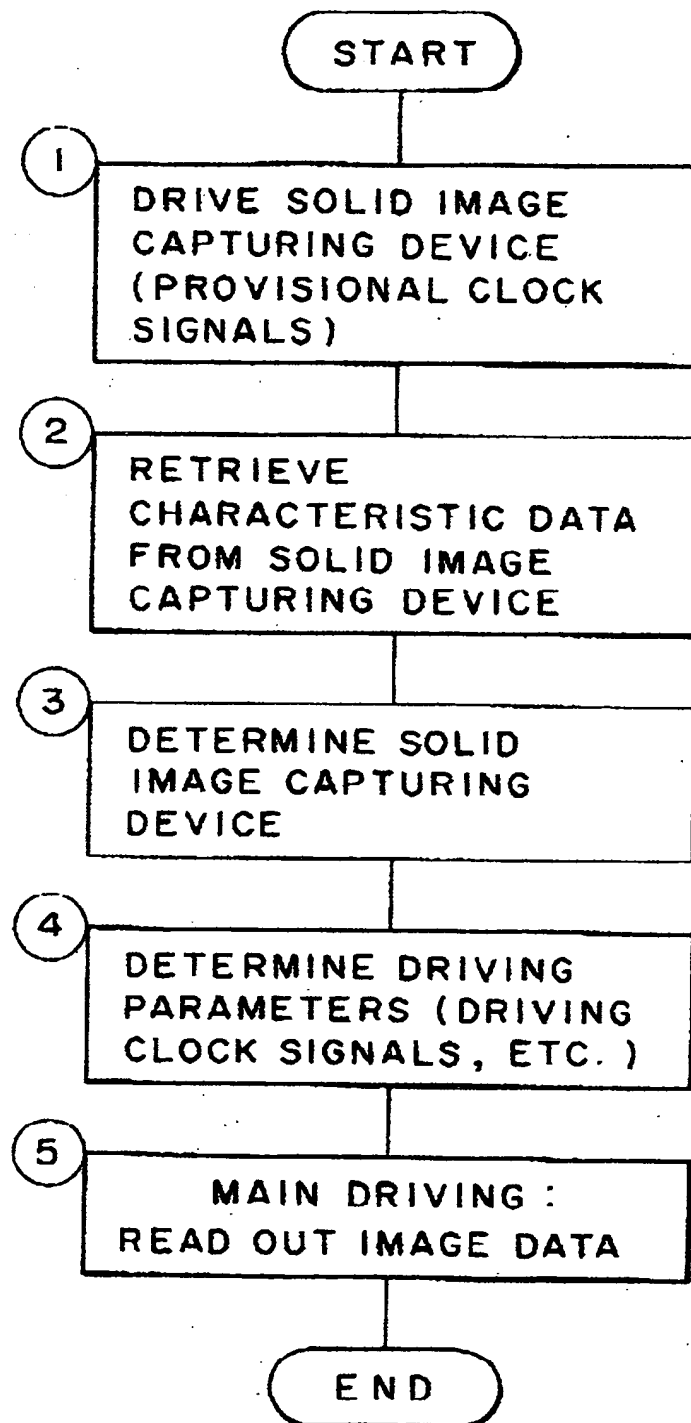


FIG. 4

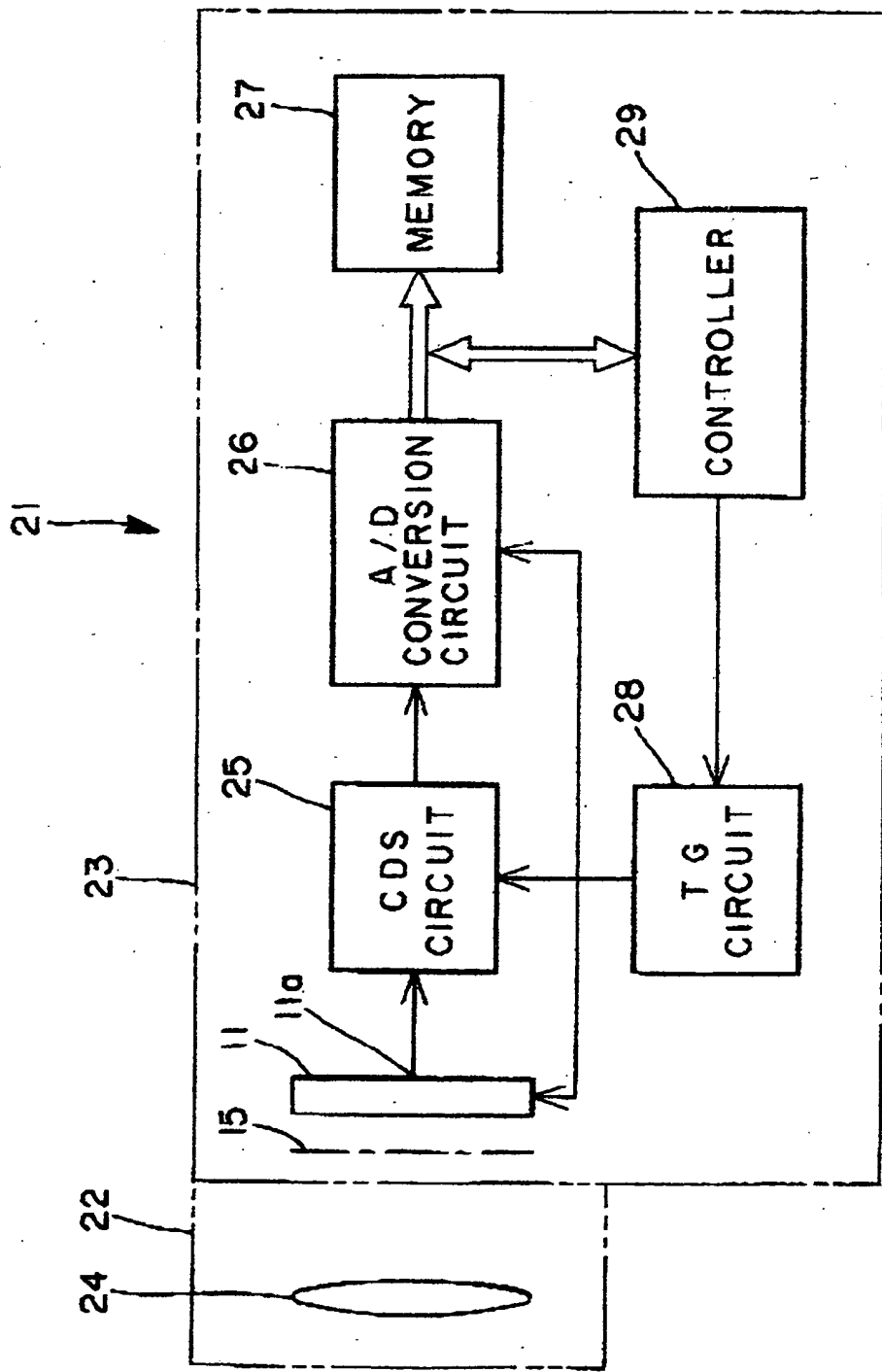


FIG. 6

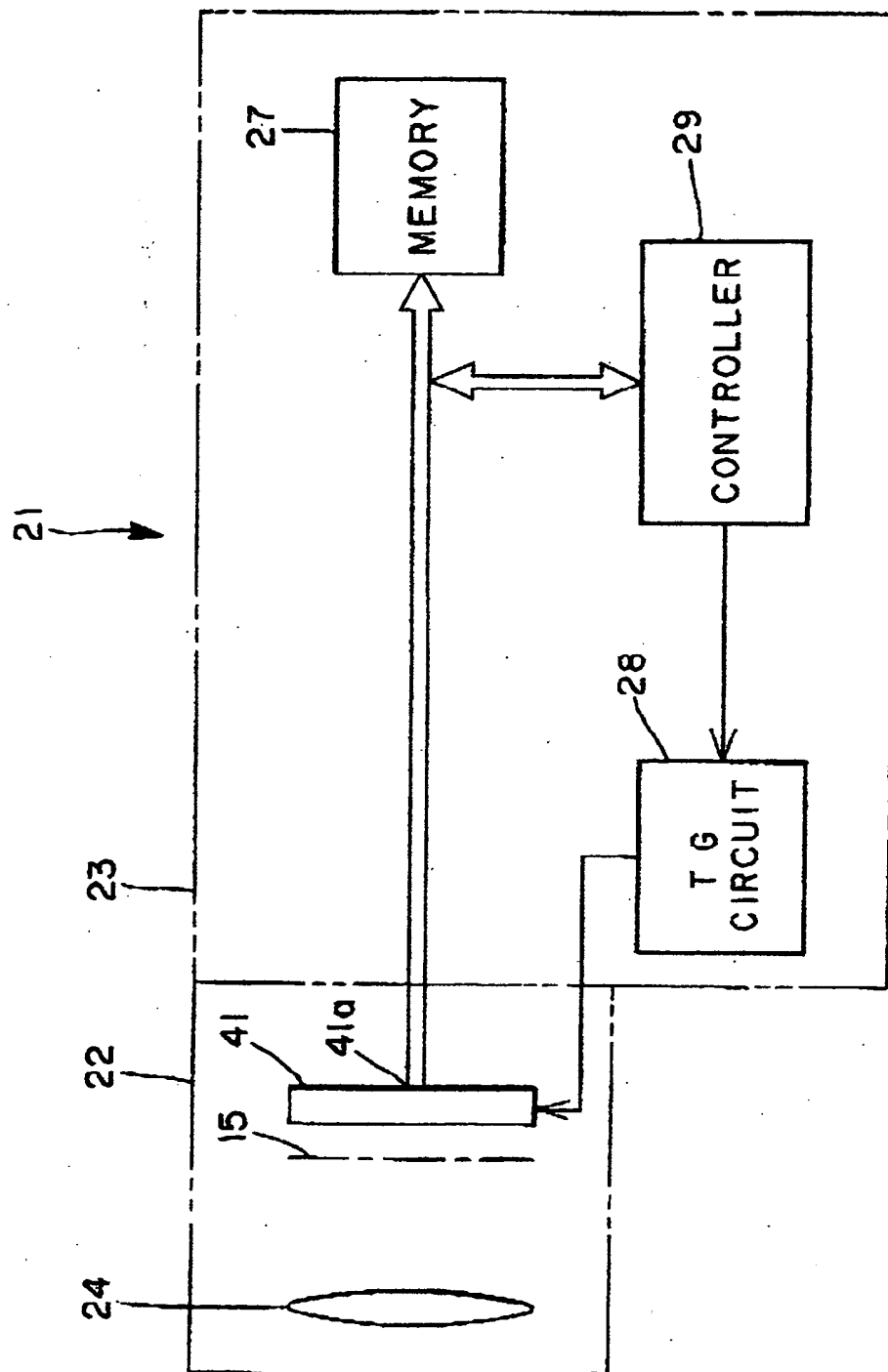


FIG. 7

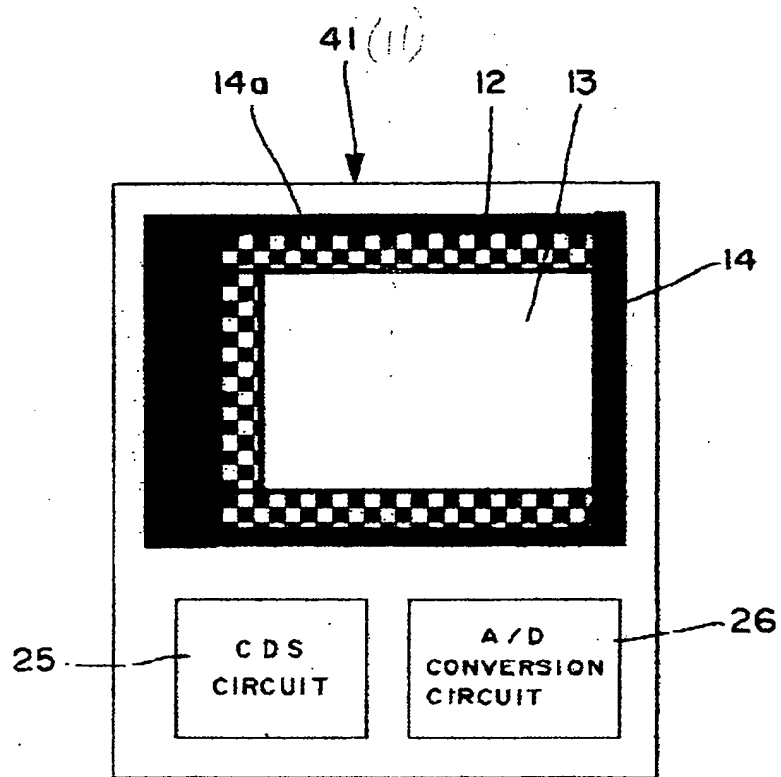


FIG. 8

WAVEFORM
REPRESENTING
OUTPUT FROM CCD



FIG. 10a

OUTPUT FROM CCD
MEMORY



FIG. 10b